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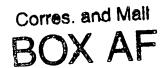
09/753,764 Application Number ETRANSMITTAL Filing Date **December 29, 2000 FORM** Sailesh B. KOTTAPALLI First Named Inventor JUL 1 5 2005 2183 Art Unit **Examiner Name** David J. HUISMAN (to be used for all∕correspondence after initial filing) Total Number of Pages in this Submission Attorney Docket Number Intel 2207/10122 ENCLOSURES (check all that apply) Drawing(s) Fee Transmittal Form After Allowance Communication to TC Appeal Communication to Board Fee Attached Licensing-related Papers of Appeals and Interferences Petition Appeal Communication to TC Amendment / Reply (Appeal Notice, Brief, Reply Brief) Petition to Convert to a X After Final Proprietary Information **Provisional Application** Power of Attorney, Revocation Affidavits/declaration(s) Status Letter Change of Correspondence Address **Terminal Disclaimer** Other Enclosure(s) **Extension of Time Request** (please identify below): 1. Postcard Request for Refund **Express Abandonment Request** CD, Number of CD(s) _ Information Disclosure ■ Landscape Table on CD Statement Remarks Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Kenyon & Kenyon Signature **Printed Name** Stephen T. Neal July 13, 2005 Reg. No. 47,815 Date CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. M/S: AF Signature Barbara Vance July 13, 2005

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Patent

Attorney Docket No.: Intel 2207/10122

Assignee: Intel Corporation Under 37 C.F.R. §1.116 Expedited Procedure Examining Group 2183



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS

Sailesh B. KOTTAPALLI

SERIAL NO.

09/753,764

FILED

December 29, 2000

FOR

METHOD AND APPARATUS FOR INSTRUCTION POINTER

STORAGE ELEMENT CONFIGURATION IN A

SIMULTANEOUS MULTITHREADED PROCESSOR

GROUP ART UNIT:

2183

EXAMINER

David J. HUISMAN

M/S: AF

Commissioner for Patents

P.O. Box 1450

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Date: July 13, 2005

Barbara Vance

AMENDMENT AND RESPONSE

Dear Sir:

The following amendment and remarks below are respectfully submitted in response to the Final Office Action dated May 13, 2005.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Amendments to the Drawings begin on page on page 8 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks/Arguments begin on page 9 of this paper.

An Appendix including amended drawings is attached after page 13 of this paper.